

X-444-2P-2US

AUG 02 2004

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THE UNITED STATES PATENT OFFICE

PATENT

404
8/12/04

Applicants: Andrew K. Percey et al.

Assignee: Xilinx, Inc.

Title: "Digital Spread Spectrum Circuitry"

Serial No.: 09/684,528 File Date: 10/06/00

Examiner: Sam K. Ahn Art Unit: 2634

Docket No.: X-444-2P-2 US Conf. No.: 1500

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

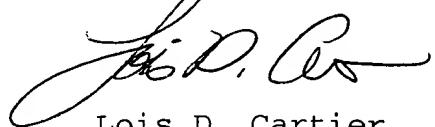
Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the one (1) reference listed in the attached Substitute for Form PTO-1449A/PTO. A copy of the reference is enclosed.

This Supplemental Information Disclosure Statement is being filed under 37 CFR 1.97(b) after to the receipt of an office action but prior to the receipt of a Notice of Allowance.

Citation of the above document shall not be construed as an admission that the document is necessarily prior art with respect to the instant invention. Citation of the above document shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above document shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

The Commissioner is hereby authorized to charge the fee of \$180.00 due under 1.17(p) and any additional fees, or credit any overpayment which may be required to Deposit Account No. 24-0040.

Respectfully Submitted,



Lois D. Cartier
Agent for Applicants
Reg. No. 40,941

I hereby certify that this correspondence is being deposited with the United States Postal Service as **first class mail** in an envelope addressed to: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on July 29, 2004.

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